

Memory Design Techniques for Low Energy Embedded Systems

by

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Synopsis

Memory Design Techniques for Low Energy Embedded Systems centers one of the most outstanding problems in chip design for embedded application. It guides the reader through different memory organizations and technologies and it reviews the most successful strategies for optimizing them in the power and performance plane.

Sort review

From the reviews:"The book covers most of the major areas of system-level design and modeling, and much of the work described has been incorporated into a commercial ESL tool This book's scope and range of pragmatic ideas make it valuable for a wide audience. ... When combined with the extensive list of references (260!), this is a very valuable resource for anyone interested in the area It should resonate with students, researchers, and practical designers" (Grant Martin, IEEE Design and Test of Computers, May-June, 2007)--This text refers to the hardcover edition.

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